



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

7/Election
7-Step to
3-26-03

In re U.S. Patent Application of

HAYASHI *et al.*

Application Number: 09/976,052

Filed: OCTOBER 15, 2001

For: LEVEL SHIFT CIRCUIT AND

SEMICONDUCTOR INTEGRATED CIRCUIT

Attorney Docket No. HITA.0110

Art Unit: 2816

Examiner:

NGUYEN, MINH T.

**Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231**

RECEIVED
MAR 20 2003
TC 2800 MAIL ROOM

RESPONSE AND AMENDMENT UNDER 37 C.F.R. §1.111

Sir:

This is in response to the Office Action dated December 27, 2002, the shortened period for response to which will expire on March 27, 2003, with the payment of the appropriate government extension fee for two months. Applicants hereby elect the continuing prosecution of Species I, drawn to a level conversion circuit, having a structure shown in figures 1, 2, 3, 7, 8, 9, 10, 11, 12 and 13, with traverse. Claims 1 through 19 pertain to the elected Species, wherein claims 1, 12, 14 and 15 are believed generic to all the identified species.